

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 57-008846

(43)Date of publication of application : 18.01.1982

(51)Int.Cl.

G06F 7/58

(21)Application number : 55-082504

(71)Applicant : FUJITSU LTD

(22)Date of filing : 18.06.1980

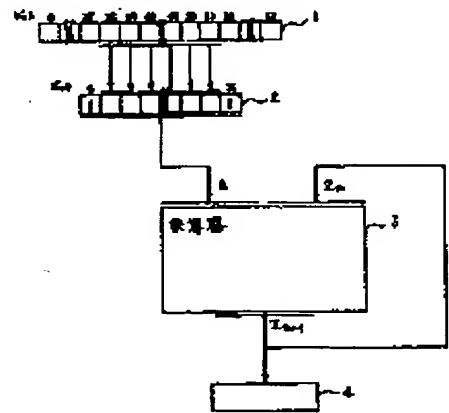
(72)Inventor : MARUYAMA FUMIHIRO

(54) PSEUDO-RANDOM NUMBER GENERATION SYSTEM

(57)Abstract:

PURPOSE: To generate pseudo-random numbers which approximate extremely to natural random numbers with simple constitution by generating the pseudo-random numbers by generating a parameter by using the sequential indication contents of a timer as a constituent element.

CONSTITUTION: A timer 1 consists of, for example, a binary counter of 64-bit constitution and increases a bit position 51 by one in every μ second to update clocking contents. In a parameter register 2, bit information 1 is set fixedly to the most significant digit bit 0 and least significant digit bit 15. In pseudo-random number generating mode, each bit information at the bit position 38 or 51 of the timer 1 is set in the intermediate bit area of the register 2 at a time to generate parameter information (a) including pieces of information 1 on the most significant digit and lest significant digit bit. A multiplier 3 multiplies the information (a) by a multiplication input value x_n obtained by feeding a multiplication output back to calculate pseudo-random number information x_{n+1} of prescribed bit constitution. This information x_{n+1} is set in a register 4 and also fed back to the multiplier 3.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]